The Trajectory approach for AFDX FIFO networks revisited and corrected
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Abstract—We consider the problem of dimensioning real-time AFDX FIFO networks with a worst-case end-to-end delay analysis. The state-of-the-art has considered several approaches to compute these worst-case end-to-end delays. Among them, the Trajectory approach has received more attention as it has been shown to provide tight end-to-end delay upper bounds. Recently, it has been proved that current Trajectory analysis can be optimistic for some corner cases, leading to its current form, to certification issues. In this paper, we first characterize the source of optimism in the Trajectory approach on detailed examples. Then, we provide a correction to the identified problems. Two problems are solved: the first one is on the definition of the time interval to consider for the worst-case end-to-end response time computation of flows at their source nodes. The second one is on the way that serialized frames are taken into account in the worst-case delay analysis.

I. INTRODUCTION

Reliability and safety are primary constraints in high-critical industrial systems such as aircraft and public transport systems. Furthermore, the needs in these systems are drastically increasing with the development of intelligent transport systems: in addition to safety requirements, we want to add comfort, usability, information management, etc.. This increase in terms of functionalities implies to exchange more and more data and resources through networks. That is why we need specific network architectures that can manage this workload.

High-critical real-time systems often rely on deterministic network architectures to exchange information between end-systems. When throughput is under concern, switched Ethernet networks are more and more considered. Several solutions have been proposed like Synchronous Ethernet (SyncE) [1], Avionics Full Duplex switched Ethernet (AFDX) [2], Flexible Time Triggered Switched Ethernet (FTT-SE) or Time Triggered Ethernet (TTEthernet) [3]. They are network architectures which are able to bound, with clock synchronization (SyncE, FTT-SE and TTEthernet) or without clock synchronization (AFDX), the end-to-end delay of a message sent through the network. In order to guarantee deterministic communications, several worst-case analysis have been proposed to compute the upper-bounds of the end-to-end communication delays of all the flows sent in the network, and to assure their reliability.

There exist different approaches for computing end-to-end delays in real-time networks. Among them, we can cite:

- The Holistic approach, is a pessimistic way (showed in [4]), and more considered as a general approach [5].

It consists of computing end-to-end delay bounds by considering a worst-case scenario (possibly unreachable) in each node visited by a flow.

- The Network Calculus approach [6], [7], which considers worst-case scenario of a flow at each visited node according to service curves. This approach has been improved by considering the serialization effect by [8].

- The Trajectory approach [9], [10], [11], [4], which consists in representing the network not just as a set of nodes, but as a set of flow trajectories through nodes and where each trajectory (associated to one flow) is a potential source of delay for the other flows. This approach is based on the computation of end-to-end delays induced by other frames in the network, and specially the one induced by their serialization on the input ports of switches (called the serialization delay). It was shown that the classical Trajectory approach provide tighter upper bounds than the holistic one but can be pessimistic. Its pessimism has been analyzed in [12].

In [13], [8], the Trajectory approach has been improved by taking into account the serialization of frames sent on the same nodes (called serialization effect in the following). Bauer&al. has shown interesting properties of worst-case delay analysis in the avionics context as it can bring slightly more accurate delay upper bounds than the classical Network Calculus approach. In this work, we focus on the Trajectory approach.

More recently in [14], a counter-example has been given, showing that in its current form the Trajectory approach can bring optimistic results for corner cases. A discussion about the source of pessimism has been given in [14], while no solution has been proposed.

The main objective of this paper is to identify the source of optimism in the Trajectory approach and to propose a solution to it. The first contribution of our work is to characterize this optimism on a detailed example with a detailed analysis. Then, we give a correction of the optimism problem first on the considered example and then by formalizing a correction in the general case. We show that the error rate of the state-of-the-art Trajectory approach w.r.t. our is higher than 10% in the examples we considered.

The paper is organized as follows. II introduces the network
model and flow model considered in the paper. An real-time AFDX network is presented. III presents the classical Trajectory approach and its improvement with the serialization effect. IV shows a counter-example where the Trajectory approach introduces optimism in the computation. V demonstrates the explicit approach and its improvement with the serialization effect. VI concludes the paper.

II. REAL-TIME AFDX NETWORK

Avionics Full Duplex Switched Ethernet (AFDX) [2] is a switched Ethernet network which has been defined for the avionics context and developed for modern aircraft such as Airbus A380. AFDX is one of the industrial applications of real-time switched Ethernet networks. The descriptions of the network and flow models are given in the following paragraphs.

A. Network model

In this paper, we study a real-time AFDX switched Ethernet network which is a network able to provide a deterministic data transmission service, through an Ethernet layer. The inputs and outputs of the network are source nodes, called End Systems (ES) in the context of AFDX network. These source nodes are interconnected by a full duplex switched Ethernet. We consider a homogeneous single network.

Each source node sends a set of flows through an output port with a buffer supporting First In First Out (FIFO) scheduling. It can be connected to only one port of a switch and each port of a switch can be connected to at most one node. Traffic shaping technique is adopted at the output port of each source node in order to guarantee a minimum interval time between two consecutive frames of a flow (also called gap in AFDX).

Each switch uses a store and forward policy. It has one buffer at each output port which supports the FIFO scheduling. It receives frames from input ports and forwards them to the corresponding output ports based on a static routing table. There is a switching latency (technological latency) to deal with the frame forwarding between an input port and an output port of a given switch and it is upper bounded by a known value $sl$.

Links between switches are full-duplex, which guarantees no collisions on links. The bandwidth (transmission rate) of the network is denoted by $R$.

A classical AFDX architecture is depicted in Figure 1. It includes six End Systems $ES_1, ES_2, ES_3, ES_4, ES_5$ and $ES_6$ interconnected by three switches $S_1, S_2$ and $S_3$ via full-duplex links.

![AFDX network example](image_url)

B. Flow model

A Virtual Link (VL) standardized by ARINC-664 is a concept of virtual unidirectional communication channel. Each VL flow $v_i$ transmitted over an AFDX network is characterized by the Bandwidth Allocation Gap (BAG$_i$), which is the minimum duration between two consecutive frames of $v_i$, as well as $l_{min}$ and $l_{max}$, which are the minimum and maximum frame lengths.

In this paper, we assume that $n$ VL flows $v_i$, $i \in \{1, 2, ..., n\}$ are transmitted over the AFDX network in order to exchange data. A VL flow $v_i$ has a path defined by a sequence of output ports $P_i = \{first_i, last_i\}$, where the $first_i$ is the source node of flow $v_i$ and $last_i$ is the last visited output port of flow $v_i$ along the path $P_i$. For instance, $v_1$ in Figure 1 follows the path $P_1 = \{ES_1, S_1, S_2\}$. Its source node is $first_1 = ES_1$. Its last visited output port is $last_1 = S_2$. This flow model can be extended to multicast flows where each path is associated to one receiver. For the sake of simplicity, we only consider unicast flows in this paper.

Sporadic VL flows are transmitted over the network. The temporal features of a given VL flow $v_i$ are defined by the following parameters:

- the minimum inter-frame duration $T_i$, which corresponds to the value of $BAG_i$ associated to flow $v_i$, and
- the worst-case transmission time (WCTT) of one frame $f_i$ of flow $v_i$. It is denoted by $C_i$ and can be computed by $C_i = \frac{l_{max}}{R}$. Indeed, instead of defining a specific WCTT for each node and each frame, we define the worst-case transmission time of a frame transmitted at all nodes.

In this paper, for the sake of simplicity, we do not consider release jitter for each flow, but a release jitter can be easily taken into account in the computation of worst case end-to-end delays.

III. UNDERSTANDING THE TRAJECTORY APPROACH

The Trajectory approach allows us to compute a bound on the worst-case transmission delay of any flow transmitted on a switched Ethernet network. It has been first proposed for the First In First Out (FIFO) scheduling in [9] and extended for non-preemptive Fixed Priority (FP) scheduling in [10], [11]. In the following paragraphs, we first recall the classical Trajectory approach principles. Then, we present the integration of serialization effect in the Trajectory approach proposed in the state-of-the-art.

A. Notations

For a flow $v_i$ following a path $P_i = \{first_i, ..., last_i\}$, we focus on a frame $f_i$ which arrives at the output port of $first_i$ at time $t$. The following notations are given for the computation.

- $h$ represents any output port of a node or of a switch in our network.
- $h + 1$ (resp. $h - 1$) represents the previous (resp. following) output port of the output port $h$. 

• $bp^h$ represents the busy period of frame $f_i$ at the output $h$. A busy period means a time interval during which there is no idle time.

• $FT_{min}$ is the smallest WCTT among the frames transmitted during $bp^h$. It is computed by $FT_{min} = \min_{h \in P_i} (C_j)$.

• $M_i^h$ is considered as the earliest arrival time of the first packet that will delay frame $f_i$ on the output port $h$. It is computed by:

$$M_i^h = \sum_{k=first_i}^{h-1} (FT_k^h + sl)$$

• $S_{max}^h$ and $S_{min}^h$ are the maximum and the minimum delay experienced by frame $f_i$ from its source node $first_i$ to the output port $h$.

• $t$ is the arrival time of a frame under study $f_i$ at the input port of node $first_i$. This arrival time should be considered w.r.t. a reference time 0, which is our time origin. Indeed, in the architectures we want to focus on, frames that do not necessarily arrive at time 0, but can be delayed and arrive later.

• We define a time interval used to compute the maximal number of frames about to delay our focused message, $A_{i,j}$, as defined in [14]:

$$A_{i,j} = S_{max}^h - S_{min}^h - M_i^h + S_{max}^j$$

Let’s illustrate these definitions by an example. We want to study the following elementary network

$$A_{i,j} = \max\{a, 0\}.$$  

Let’s illustrate these definitions by an example. We want to study the following elementary network

![Diagram](image)

We consider that the first input port of $S$ receives two different flows, $v_1$ and $v_3$. $v_1$ emits a frame $f_1$, with $C_1 = 30$ and $T_1 = 4000$, and $v_3$ sends $f_3$, with $C_3 = 20$ and $T_3 = 4000$. Following the same way, the flow $v_2$ sends only one packet through the network, called $f_2$, with $C_2 = 40$ and $T_2 = 90$. We want to focus on the packet $f_2$, which arrives on $ES_1$ at time $t = 20$. Considering this system, we can represent its behavior with the figure 3.

### B. Calculating an end-to-end delay

Actually, the computation of the Trajectory approach in FIFO context is summarized by the combination of 4 different terms. Combining these terms allows us to calculate, for a specific frame $f_i$ released at time $t$, of a flow $v_i$, the latest starting time $W_{i,t}^{last_i}$: from its last visited output port $last_i$, corresponding to the last node of the network belonging to $P_i$. We have the following expression:

$$W_{i,t}^{last_i} = \sum_{j \in \{1, \ldots, n\}} \left(1 + \left\lceil \frac{t + A_{i,j}}{T_j} \right\rceil \right)^+ \cdot C_j \quad (2)$$

$$+ \sum_{h \in P_i / \{last_i\}} \left( \max_{j \in \{1, \ldots, n\}} (C_j) \right)$$

$$+ \left( |P_i| - 1 \right) \cdot sl \quad (3)$$

$$- C_i \quad (4)$$

- Term 2 is the delay due to competing flows which delay $v_i$ along its trajectory as well as the transmission delay generated by $v_i$ itself.

- $A_{i,j}$ enters in the composition of the term $\left(1 + \left\lceil \frac{t + A_{i,j}}{T_j} \right\rceil \right)^+$ which is the maximal number of frames generated by a flow $v_j$ that can delay flow $v_i$.

- To evaluate the end-to-end delay of a flow $v_i$, we need the value of $A_{i,j}$ for all encountered flow $v_j$. To do this, we first calculate the two terms $S_{max}^h$ and $M_i^h$ which are the same for each flow $v_j$. Respectively, $S_{max}^h$ is the earliest arrival time of the first packet which delays $i$ on a node $h$, and $S_{min}^h$ (see Figure 3).

- Term 3 is the transition cost from one busy period to the next one. When each frame sequence is transmitted from one busy period to the following one, there is a transition delay which is taken as the largest frame transmission time in the frame sequence. For example, in Figure 4 the frame sequence at node $ES_1$ is composed by frames $f_0$ and $f_1$. When they are transmitted to node $S$, the transmission time of frame $f_0$ is taken as the transition cost since it is larger than that of frame $f_1$.

- Term 4 is the switching latencies along the considered path, considered as a worst-case constant, identical for each link. For each encountered node we add a switching latency to the end-to-end delay of a message $f_i$, corresponding to electronic delay. So, in an entire network, the induced latency corresponding
to $f_i$ corresponds to $sl$, multiplied by the number of encountered nodes, represented by $|P_i|$. As we consider the path from the first switch to the last one of the network, we need to minus our end-to-end delay of $1 + sl$; given the point that we want to evaluate the end-to-end delay of a flow in a network, we can focus on the delay between its entry point and the output of the last switch in the network. In figure 1, this latency corresponds to the delay between $S_2$ and $ES_0$.

- Term 5 is subtracted because $W^{last_i}_i$ is the latest starting time at $last_i$. Indeed, $W^{last_i}_i$ corresponds to the delay between time $t$ and the beginning of the transmission of $i$ in $last_i$(and not the end of the transmission time). So, the value of $C_{last_i}$ needs to be subtracted from the value of $W^{last_i}_i$.

From the latest starting time, the worst-case end-to-end delay upper bound of the flow $v_i$ calculated by the Trajectory approach is obtained by [14]:

$$R_i = \max_{0 \leq j \leq t} \left\{ W^{last_i}_i + C_i - t \right\}$$  \hspace{1cm} (6)

where $B_i = \sum_{j \in \{1, n\} : P_i \cap P_j \neq \emptyset} \left[ \frac{B_j}{v_j} \right] \cdot C_j$. (see Figure 4)

$B_i$ represents the largest possible length of the busy period in each encountered node of the network.

![Fig. 4. Calculating the latest starting time of $i$ with the trajectory approach](image)

### C. The serialization of frames

The classical Trajectory approach considers that competing frames of different flows can arrive at the output port at the same time. However, for frames transmitted from the same input link, their transmissions are necessarily serialized (physical constraint) and they cannot arrive at the output port at the same time. In the following paragraphs, the serialization effect and its solution to the Trajectory approach proposed in [8] are briefly recalled.

The frame serialization exists at each switch $h$ as illustrated in Figure 5. Flows crossing an output port $h$ are transmitted from $k_h + 1$ input links $IP^h_k$ ($k \in \{0, ..., k_h\}$). For a frame $f_i$ of flow $v_i$, it crosses $h$ from the input link $IP^h_0$, which is the output link $OP^{h-1}$, to the output link $OP^h$. There are other $k_h$ input links $IP^h_k$ ($k \in \{1, ..., k_h\}$) which transmit competing frames to the output link $OP^h$. These frames can delay frame $f_i$ in the busy period $bp^h$.

![Fig. 5. Illustration on an output link $OP^h$ and $k_h + 1$ input link $IP^h_k$](image)

In [8], an optimization on the Trajectory approach has been proposed by taking into account the serialization effect. An illustration of the optimization is shown in Figure 6. Due to the FIFO scheduling at the output buffer of $h$, any frame arriving later than the arrival time of frame $f_i$ ($\theta$ in Figure 6) at node $h$ cannot delay $f_i$. Then the delay of $f_i$ is maximized by postponing the last frame arrival of each frame sequence from each input link $IP^h_k$ ($k \in \{1, ..., k_h\}$) till the time $\theta$. It has been illustrated in [8] that at the output port $OP^h$, there can be frames transmitted before the first frame arrival from $IP^0$. These frame transmissions do not delay frame $f_i$ and should be taken into account in the delay computation. The associated duration is denoted by $\Delta^h_{i,t}$.

From [8], the value of $\Delta^h_{i,t}$ is minimized when the first frame of $IP^h_0$ is the smallest frame transmitted by $IP^0$ and the first frame of $IP^h_k$ is the largest frame transmitted by the corresponding input link $IP^h_k$. The scenario is illustrated in Figure 6 and the computation is given by:

$$\Delta^h_{i,t} = \max_{x \in \{1, ..., k_h\}} \left\{ \sum_{v_j \in IP^h_x} \left( \max_{C_j} \left( 1 + \frac{t + \frac{A_{i,j}}{T_j}}{T_j} \right) \cdot C_j \right) - \min_{v_j \in IP^h_0} (C_j) \right\}$$

Then serialization factor at each visited output port along the studied path $P_i$ is given by:

$$\sum_{h \in P_i / \{ first_i \}} (\Delta^h_{i,t})$$  \hspace{1cm} (7)

From [8], it has been shown that $W^{last_i}_i$, including the serialization factors is given by:

$$W^{last_i}_i = \sum_{j \in \{1, ..., k_h\} : P_i \cap P_j \neq \emptyset} \left( 1 + \left[ \frac{t + \frac{A_{i,j}}{T_j}}{T_j} \right] \right) \cdot C_j$$

$$+ \sum_{h \in P_i / \{ first_i \}} \left( \max_{C_j} (C_j) \right)$$

$$+ ((|P_i| - 1) \cdot sl - \sum_{h \in P_i / \{ first_i \}} (\Delta^h_{i,t}) - C_i)$$
This formula has been shown to be optimistic in particular case [14]. We will now illustrate why it can be optimistic.

IV. PROBLEM: COUNTER-EXAMPLE WITH OPTIMISM

The Trajectory approach considers the worst-case scenario that can happen to a frame along its trajectory in order to guarantee the delay upper bound computation. Recently, a counter-example has been shown in [14] to point out that for some corner cases, the Trajectory approach can lead to optimistic computed delay upper bounds. This example is given in Figure 7.

In the case of frame $f_1$, since it is the only frame transmitted from $ES_1$, we have $M_{S_1}^{S_3} = t + S_{S_3}^{S_3} = 40 \mu s$, giving the interval $[M_{S_1}^{S_3}, t + S_{S_3}^{S_3}]$ which leads to only a time instant $40 \mu s$. It means that for flows $v_2$, $v_3$, and $v_4$ competing with flow $v_1$ at the output port of $S_3$ (port 1 in Figure 7), only the frames arriving at this output port at time $t = 40 \mu s$ can delay frame $f_1$. Therefore, for each flow there is at most one frame that can delay frame $f_1$, among frame $f_2$, frame $f_3$, and frame $f_4$ as shown in Figure 9.

However, as shown in Figure 8, frames $f_3'$ and $f_4'$ which can actually delay frame $f_1$ are not taken into account since they arrive before time $M_{S_3}^{S_3} = 40 \mu s$. Therefore, the interval $[M_{S_1}^{S_3}, t + S_{S_3}^{S_3}]$ is not correct (under-estimated). Indeed, in the example $t = 0$ does not lead to the worst-case scenario.

Actually, for $t = 40 \mu s$, the Trajectory approach computes a longer interval $[M_{S_1}^{S_3}, t + S_{S_3}^{S_3}] = [40, 80]$ as shown in Figure 10. In this case, both frames $f_3$ and $f_4'$ as well as frames $f_4$ and $f_4'$ are taken into account in the computation. However, the computation still gives an optimistic result which is $R_1(t = 40) = 140 \mu s$.

The computation subtracts a serialization factor $\Delta_{S_1}^{S_3} = 40 \mu s$ (Term 7) since the transmission of frame $f_3'$ at $S_3$ does not delay the frame $f_1$ due to the serialization effect. Meanwhile, a time $t = 40 \mu s$ is also subtracted in Formula 6 since frame $f_1$ arrives at time $t$ and therefore the time interval $[0, t]$ is not part of the delay of frame $f_1$. In fact, these two parts of subtractions overlap and the same time interval is subtracted twice from the computation (detailed explanation will be given in the next section). It then results in an optimistic computed delay $140 \mu s$ of frame $f_1$, as illustrated in Figure 10.

In the next paragraphs, a detailed analysis of the optimism will be given and a solution to the optimism problem will be proposed.
A correction of the Trajectory approach

As shown in the previous paragraphs, for a frame $f_i$ of a flow $v_i$, the problem of the under-estimated interval $[M_i^b, t + S^{b}_{max,i}]$ disappears with the examinations of all the possible values of $t$. However, at the same time subtracting time $t$ from the delay of frame $f_i$ (Equation 6) can overlap with part of the serialization factor (Term 7). The following paragraphs first examine the optimism of the Trajectory approach by an illustrative example, with both the classical approach and the serialization optimization respectively. Then the analysis is developed in the general case and a correction of the optimism problem is proposed.

A. Computation with the classical approach

In order to explain this problem in details, let us consider the example given in Figure 1. The flow temporal characteristics are given in Table I. In the example, the switching latency is considered as null.

<table>
<thead>
<tr>
<th>$v_1$</th>
<th>$v_2$</th>
<th>$v_3$</th>
<th>$v_4$</th>
<th>$v_5$</th>
<th>$v_6$</th>
<th>$v_7$</th>
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</table>

Table I. Flow temporal characteristics of the network example

Frame $f_1$ of flow $v_1$ is under study. It follows the path $P_1 = \{ES_1, S_1, S_2 \}$ and it can be delayed by frames of flows $v_2$ and $v_3$ at the output port of $S_1$, as well as by flows $v_4, v_5, v_6, v_7$ and $v_8$ at the output port of $S_2$.

One worst-case scenario of frame $f_1$ is given in Figure 11 when $t = 120 \mu s$. The worst-case delay of frame $f_1$ is $320 \mu s$. As illustrated in this figure, flow $v_2$ has two frames $f_2$ and $f_2'$ delaying frame $f_1$ at the output port of $S_1$, and flow $v_3$ has two frames $f_4$ and $f_4'$ delaying frame $f_1$ at the output port of $S_1$.

Let us now consider the Trajectory approach. Since flow $v_1$ is the only flow emitted by $ES_1$, then $M_1^{S_1} = 40 \mu s$ and $S_{max}^{S_1} = S_{min}^{S_1} = C_1 = 40 \mu s$. First, for each flow encountered by $v_1$, we need to calculate the value of $A_{i,j}$. For this, we apply the Equation 1. Flow $v_2$ is emitted by $ES_2$ and can be delayed by frames of flow 9 and then it can be delayed by frames of flow $v_3$ at the output port of $S_1$, which gives $S_{max}^{S_1} = 160 \mu s$ and $S_{min}^{S_1} = 80 \mu s$. Therefore, the computation gives $A_{1,2}$ as follows:

$$A_{1,2} = S_{max}^{S_1} - S_{min}^{S_1} - (M_1^{S_1} - S_{max}^{S_2})$$

$$= 40 - 80 - (40 - 160)$$

$$= 80 \mu s$$

In the same way, the values of $A_{1,3} = 40 \mu s$ and $A_{1,4} = A_{1,5} = A_{1,6} = A_{1,7} = A_{1,8} = 200 \mu s$ are obtained. So, we obtain the following results:
First, let us consider the classical Trajectory approach without serialization factor. In this case, the computation is done based on pessimistic assumption that all the frames of different flows arrive at the same time even if they are serialized by some input link (the serialization factor is not considered).

It has been illustrated in Figure 12 that although frames \( f'_4, f_3, f_6, f_7 \) and \( f_8 \) are considered arriving at the same time, frames \( f_6, f_7 \) and \( f_8 \) do not delay frame \( f_1 \). Indeed, the computation of \( W_{S2}^{ES_1} \) is based on the maximized busy period \( b_p^{S2} \) which starts at time \( M_1^{S2} \) (shown in Figure 12). Since frame \( f_1 \) arrives at \( ES_1 \) at time \( t \), there is a time interval of \( t \) which does not contribute to the delay of frame \( f_1 \). It means that during the busy period \( b_p^{S2} \), the frame transmission during \( [M_1^{S2}, M_1^{S2}+480] \) do not delay frame \( f_1 \), which are exact the frames \( f_6, f_7 \) and \( f_8 \) in the example.

Suppose that the delay upper bound of frame \( f_i \) is obtained when \( f_i \) arrives at \( fist \) at a given time \( t \). The corresponding scenario of \( R_i \) is illustrated by the first bar in Figure 13 where the time interval \( t \) is subtracted since it is not part of delay. Then the shadow part in the bar represents the delay.

Since the classical Trajectory approach considers that the competing frames arrive after time \( M_1^b \) at each node \( h \) and there is no frame transmitted before \( M_1^b \), then the subtrac-
tion of the time interval $t$ in Formula 6 is equivalent to the subtraction of the WCTT of frames transmitted between $[M_{i}^{last_i}, M_{i}^{last_i} + t]$ at the last visited node $last_i$. The corresponding scenario is also illustrated by the second bar in Figure 13 where the time interval $t$ (white bar) is moved between $M_{i}^{last_i}$ and $M_{i}^{last_i} + t$.

### B. Computation with Serialization effect

As explained in Section III-C, frames transmitted from the same input link are serialized and cannot arrive at the output at the same time. Therefore, the scenario shown in Figure 12 where frames $f'_1$, $f_5$, $f_6$, $f_7$ and $f_8$ arrive at $S_2$ at the same time is impossible and pessimistic.

Indeed, as shown in Figure 11, at the output port of $S_1$, frames $f_2$, $f'_2$ and $f_3$ arrive from the same link $\{S_3, S_1\}$. Since frame $f_1$ is the only frame transmitted from $ES_1$, only one frame from the input link $\{S_3, S_1\}$ can delay frame $f_1$. In this illustration, it is frame $f_2$. Then the serialization factor is computed by $\Delta S_{1,2} = C_3 + C_2 = 80 \mu s$. Similarly, at the output port of $S_2$, the serialization factor is computed by $\Delta S_{2} = 80 \mu s$. Then by taking into account the serialization effect, we have $W_{S_{1,2}} = 440 - \Delta S_{1,2} = 280 \mu s$ and $R_{i}(120) = W_{i}^{S_{2}} + C_1 - t = 200 \mu s$, which is optimistic compared to the worst-case delay $320 \mu s$ in Figure 11.

The optimism is introduced since the subtraction of serialization factors is partially overlapped with the subtraction of the time interval $t$ in Equation 6. The classical approach subtracts the transmissions of frames $f_6$, $f_7$ and $f_8$ by the subtraction of time $t$ as shown in Figure 12, while the serialization factors subtract the transmissions of frames $f_6$, $f_7$ and $f_8$ as illustrated in Figure 11. In this case, the frames $f_6$, $f_7$ and $f_8$ are actually subtracted twice and therefore leading to the optimistic result.

In the general case, the serialization factors subtract frame transmissions of $\sum_{h \in P_{i,t}^{first_i}} \Delta_{i,t}$ from the computation at the last visited output $last_i$. For the purpose of illustration, we first consider the scenario in Figure 6. The starting instant of the busy period $b^h$ is denoted $SBp^h$ as shown in Figure 6. Since the input $IP_0^h$ is the output busy period $b^{h-1}$ in the previous output port $h - 1$, then $SBp^{h-1}$ is also indicated in Figure 6. Therefore, we have:

$$SBp^h = SBp^{h-1} + FT_{min}^{h-1} + sl - \Delta_{i,t}$$

At the source node $first_i$, the worst-case scenario is when all the frames arrive at the same time as frame $f_i$ and delay $f_i$. Then $SBp^{first_i}$ is equal to the arrival time of $f_i$, i.e. $SBp^{first_i} = t$. Based on the Equation 8, the computation propagates till $last_i$ where we have:

$$SBp^{last_i} = t + \sum_{h \in P_{i,t}^{last_i}} (FT_{min}^{h} + sl) - \sum_{h \in P_{i,t}^{last_i}} \Delta_{i,t}^h$$

This scenario is illustrated in Figure 14 which shows that at the last visited output $last_i$, the frame transmissions between $SBp^{last_i}$ to $t + M_{i}^{last_i}$ are subtracted from the computation.

In the given example, we want to evaluate the error rate induced by the optimism in the Trajectory approach (compared to the standard hand-built method). So, we make the following evaluation:

$$E(ErrorRate) = (320 - 280) * 100/320 = 12.5\%.$$
reliability and accuracy, such a high error rate directly corresponds to invalidating the method of the Trajectory approach. That is why we need to find a solution to this optimism.

C. A solution to optimism

Let us focus on the comparison between Figure 13 where a time $t$ is subtracted from $M_{i}^{last} + t$ and Figure 14 where the serialization factors are subtracted from $Sbp_{i}^{last} + t$. These exists an overlapped time interval $[\max(M_{i}^{last}, Sbp_{i}^{last}), M_{i}^{last} + t]$ which is subtracted twice in the computation of delay upper bounds, and therefore leading to the optimistic results. In the example, this time interval is $[80, 200]$ as shown in Figure 11.

One solution to solve this problem is to take into account the subtraction of time $t$ in the serialization factors. More precisely, instead of subtracting the time of frame transmissions before time $t + M_{i}^{last}$, consider to subtract it at the last visited node the frame transmissions before $M_{i}^{last}$ since only this part is not considered by the classical Trajectory approach.

**Theorem 1.** The serialization effect in the Trajectory approach is taken into account by a serialization correction:

$$
\left( \sum_{h \in P_{i}} (\Delta_{h,t}^{i}) + t \right)^{+} \tag{9}
$$

**Proof:** The serialization correction considers the serialization effect by taking the difference between the serialization factors and the subtracted time $t$. More precisely, two cases are considered and illustrated below.

First, we consider the case $\sum_{h \in P_{i}} (\Delta_{h,t}^{i}) \leq t$, and the corresponding scenario is illustrated in Figure 15. Since the overlapped time interval $[Sbp_{i}^{last}, M_{i}^{last} + t]$ is covered by the time interval $[M_{i}^{last}, M_{i}^{last} + t]$, the serialization effect is taken into account by subtracting time $t$ in Formula 6. Therefore, the serialization correction is taken as value 0.

Second, we consider the case $\sum_{h \in P_{i}} (\Delta_{h,t}^{i}) > t$, and the corresponding scenario is illustrated in Figure 16. The frame transmissions before $M_{i}^{last}$ do not delay frame $f_{i}$, and their time needs to be subtracted, which corresponds to the serialization correction. The rest part of the serialization factors is equal to the overlapped time interval $[M_{i}^{last}, M_{i}^{last} + t]$ and it is subtracted in Formula 6.

Therefore, the Term 9 is a correction of Term 7 of serialization factors.
As it has been demonstrated, the overlapped time interval
\[ \max([S_{i,1,20}^h, S_{h,1,20}^h], M_{i,1,20}^h + t) \] is subtracted twice in the computation and it can lead to optimistic result of delay upper bound. The optimism introduced by the Trajectory approach presented in III is then computed by:

\[
\min(\sum_{h \in P \setminus \text{first}_i} \Delta_{h_i,t}^h, t)
\]

The overlapped time interval is equal to 0 only when \( t = 0 \) or when \( \sum_{h \in P \setminus \text{first}_i} \Delta_{h_i,t}^h = 0 \).

In the example, the value of the serialization correction is given by:

\[-((\Delta_{1,120}^S + \Delta_{1,120}^S) - t) = -((80 + 80) - 40) = -40 \mu s\]

It is illustrated by the transmission time of frame 8 in Figure 11. Thus, the \( W_{1,120}^S \) computed by the Trajectory approach with the serialization correction is:

\[ W_{1,120}^S = 440 - 40 = 400 \mu s \]

The delay upper bound of frame 1 is then obtained by:

\[ R_1 = 400 + 40 - 120 = 320 \mu s \]

which is the exact worst-case delay of frame 1 in the example.

VI. CONCLUSION

In this paper, we have considered the problem of computing worst-case end-to-end delays of flows sent on an AFDX FIFO network. We considered the Trajectory approach, known to provide tight worst-case end-to-end delay upper bounds. Recently, it has been shown that this approach can lead to optimistic end-to-end delays thus leading to certification issues. Our goal was to characterize this optimism problem and to provide a solution to it.

We presented the different sources of optimism in the Trajectory approach on pathological examples, including the underestimation of the release time interval used to compute the worst-case end-to-end delay of a flow and a problem on the computation of the serialization factors for flows sent on the same link. The error rate found in our examples can reach 10%, a significant error. We then solved the optimism problem in the general case. As a perspective, we would like to compute the average error rate for random sets of flows.

REFERENCES